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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|---|-------------|----------------------|---------------------|------------------|
| 10/719,357 | 11/21/2003 | Harold S. Crafts | 000939-052180US | 2329 |
| 20350 | 7590 | 05/17/2005 | EXAMINER | |
| TOWNSEND AND TOWNSEND AND CREW, LLP TWO EMBARCADERO CENTER EIGHTH FLOOR SAN FRANCISCO, CA 94111-3834 | | | TAT, BINH C | |
| | | | ART UNIT | PAPER NUMBER |
| | | | 2825 | |

DATE MAILED: 05/17/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

| | | | |
|------------------------------|--------------------------------------|--|--|
| Office Action Summary | Application No. 10/719,357 | Applicant(s) CRAFTS, HAROLD S. | |
| | Examiner Binh C. Tat | Art Unit 2825 | |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 07 February 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 14-16 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 14-16 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 21 November 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. This office action is in response to application 10/719357 filed on 11/21/03.

Claims 14-16 remain pending in the application.

Response to Arguments

Applicant's arguments with respect to claims 14-16 have been considered and are

Persuasive. However, new grounds of rejection are introduced below.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 14-16 are rejected under 35 U.S.C. 102(e) as being anticipated by Kikushima et al. (US Patent 5410173).

1. As to claim 14 Kikushima et al. teach a integrated circuit, comprising: a) a standard cell array comprising rows of cells having a row pitch (see fig 5-10 and 12 col 2 lines 62 to col 3 lines 35 col 16 lines 66 to col 17 lines 47 and col. 22 lines 6 to col 23 lines 56); and b) a MACRO, embedded within the standard cell array, comprising rows of cells having pitch substantially equal to the row pitch (see fig 1, 2, 3, 5-10 and 12 col 10 lines 53 to col 11 lines 32 and col 16 lines 66 to col 19 lines 47).

2. As to claim 15 Kikushima et al. teach a integrated circuit, comprising: a) a standard cell array region, comprising rows of cells having a row pitch (see fig 5-10 and 12 col 2 lines 62 to

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col 3 lines 35 col 16 lines 66 to col 17 lines 47 and col. 22 lines 6 to col 23 lines 56); b) a pair of power conductors extending across each row (see fig 1, 2, 3, 5-10 and 12 col 10 lines 53 to col 11 lines 32 and col 16 lines 66 to col 19 lines 47); and c) a MACRO region embedded within the standard cell array region comprising rows of cells having a pitch equal to said row pitch (see fig 1, 2, 3, 5-10 and 12 col 10 lines 53 to col 11 lines 32 and col 16 lines 66 to col 19 lines 47).

3. As to claims 16 Kikushima et al. teach a integrated circuit, comprising: a) cells of a standard cell array (see fig 5-10 and 12 col 2 lines 62 to col 3 lines 35 col 16 lines 66 to col 17 lines 47 and col. 22 lines 6 to col 23 lines 56); b) cells of a MACRO, embedded in the standard cell array, having a row pitch substantially the same as the standard cell array (see fig 1, 2, 3, 5-10 and 12 col 10 lines 53 to col 11 lines 32 and col 16 lines 66 to col 19 lines 47); and c) parallel power conductors running adjacent all of said cells (see col 7 line 38-66 and fig 2a-2c col 12 lines 52 to col 13 lines 63).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Binh C. Tat whose telephone number is (703) 305-4855. The examiner can normally be reached on 7:30 - 4:00 (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mathew Smith can be reached on (703) 308-1323. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Binh Tat
Art Unit 2825
May 13, 2005

Thuan Do
THUAN DO
Primary examiner
05/16/2005